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JC971 U.S. P70
09/844532

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		ISSUE CLASSIFICATION
	Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

SA	O.I.P.E.	PATENT DATE
SCANNED	<i>FC 3/2 Q.A.</i>	<i>AC</i>

APPLICATION NO. 09/844532	CONT/PRIOR D	CLASS 324 19	SUBCLASS 7125	ART UNIT 2929 3229	EXAMINER chang
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ANTS

Test interconnect for bumpable semiconductor components and methods of fabrication

PTO-2040
12/99

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
	(Assistant Examiner)	(Date)			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				ISSUE FEE	
	(Primary Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner)	(Date)			

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